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**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2186**

zw
AF
2186

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/027,359
Filed: December 19, 2001
Inventor:
Fay Chong, Jr.

Examiner: Tsai, Sheng Jen
Group/Art Unit: 2186
Atty. Dkt. No: 5681-05200

Title: Cache Accumulator
Memory for Performing
Operations on Block
Operands

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative

[Signature]
Signature

May 26, 2005
Date

**RESPONSE TO OFFICE ACTION OF
MARCH 28, 2005**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of March 28, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.